

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: N. FUKASAWA et al.

Serial Number: 09/029,608

Filed: May 15, 1998

Group Art Unit: 2814

Examiner: Unassigned

For: **METHOD AND MOLD FOR MANUFACTURING SEMICONDUCTOR DEVICE,  
SEMICONDUCTOR DEVICE, AND METHOD FOR MOUNTING THE DEVICE**

POWER TO INSPECT

Assistant Commissioner for Patents  
Washington, D.C. 20231

January 24, 2000

Dear Sir:

The undersigned attorney of record in connection with the above-identified application,  
hereby grants:


Jonathan R. Smith & Lorreta A. Grasso  
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full power to inspect and make copies of all papers relating to the said application.

Respectfully submitted

ARMSTRONG, WESTERMAN, HATTORI, McLELAND & NAUGHTON

  
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Reg. No. 32,878

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*Approved 2/24/00  
J. Grasso*